

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09901930	
	Filing Date		2001-07-09	
	First Named Inventor	Jae-Yoon SIM		
	Art Unit	2816		
	Examiner Name	My Tran Ton		
	Attorney Docket Number	9898-204		

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	1	TANAKA, HITOSHI, et al., A Precise On-Chip Voltage Generator for a Gigascale DRAM with a Negative Word-Line Scheme, IEEE Journal of Solid-State Circuits, August, 1999, pp. 1084-1090, Vol. 34, No. 8	<input type="checkbox"/>

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☐ See attached certification statement.

☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☐ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Hosoon Lee/	Date (YYYY-MM-DD)	2007-01-12
Name/Print	Hosoon Lee	Registration Number	56,737

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